



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Glenn J. Leedy
Application No. : 10/672,961 Confirmation No.: 9439
Filed : September 26, 2003
For : THREE DIMENSIONAL MULTI LAYER MEMORY AND
CONTROL LOGIC INTEGRATED CIRCUIT
STRUCTURE
Group Art Unit : 2822
Examiner : Monica Lewis

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97, applicant wishes to call the attention of the Examiner to the following documents:

Foreign Patent Documents

DE 3233195	03/17/1983	Mitsubishi Electric Corporation
JP 2037655	02/07/1990	Siemens AG
JP 3127816	05/30/1991	Canon KK
JP 3174715	07/29/1991	Fujitsu Limited
WO 98/19337	05/07/1998	Trusi Technologies, LLC

Nonpatent Literature Documents

Wolf, Stanley and Richard N. Tauber; Silicon
Processing For the VLSI Era, Volume 1: Process
Technology; Sunset Beach, CA: Lattice Press,
1986, pages 191-194

05/01/2007 AWONDAF1 00000036 061075 10672961

01 FC:1006

100.00 DA



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Glenn J. Leedy
Application No. : 10/672,961 Confirmation No.: 9439
Filed : September 26, 2003
For : THREE DIMENSIONAL MULTI LAYER MEMORY AND
CONTROL LOGIC INTEGRATED CIRCUIT
STRUCTURE
Group Art Unit : 2822
Examiner : Monica Lewis

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97, applicant wishes to call the attention of the Examiner to the following documents:

Foreign Patent Documents

DE 3233195	03/17/1983	Mitsubishi Electric Corporation
JP 2037655	02/07/1990	Siemens AG
JP 3127816	05/30/1991	Canon KK
JP 3174715	07/29/1991	Fujitsu Limited
WO 98/19337	05/07/1998	Trusi Technologies, LLC

Nonpatent Literature Documents

Wolf, Stanley and Richard N. Tauber; Silicon Processing For the VLSI Era, Volume 1: Process Technology; Sunset Beach, CA: Lattice Press, 1986, pages 191-194

European Search Report for Application
No. EP 02 00 9643 (date completed:
October 8, 2002)

Phys. Rev. B., Condens. Matter Mater. Phys.
(USA), Physical Review B (Condensed Matter and
Materials Physics), 15 March 2003, APS through
AIP, USA.

S. Wolf, Silicon Processing For the VLSI Era,
1990, Lattice Press, Volume 2, page 191.

Svechnikov, S.V.; Kobyl'yatskaya, M.F.;
Kimarskii, V.I.; Kaufman, A.P.; Kuzolev, Yu.
I.; Cherepov, Ye. I.; Fomin, B.I.; " Switching
Card With Aluminum Membrane Current Lead
Crossovers "; 1972

Salazar, M.; Wilkins, C.W., Jr.; Ryan, V.W.;
Wang, T.T.; "Low stress films of cyclized
polybutadiene dielectrics by vacuum annealing";
Oct. 21-22, 1986; pp. 96-102.

Allen, Mark G.,: Senturia, Stephen D.;
"Measurement of polyimide interlayer adhesion
using microfabricated structures"; 1988;
pp. 352-356.

Kochugova, I.V.; Nikolaeva, L.V.; Vakser, N.M.,
(M.I. Kalinin Leningrad Polytechnic Institute
(USSR); "Electrophysical investigation of thin-
layered inorganic coatings"; 1989; pp. 826-828.

The aforementioned references are listed on the
accompanying Form PTO/SB/08 (submitted in duplicate). Pursuant to
37 C.F.R. 1.98 (a)(2), copies of the aforementioned Foreign Patent
Documents and Non-Patent Literature Documents are enclosed
herewith.

Applicant reserves the right to establish the
patentability of the claimed invention over any of the information
provided herewith, and/or to prove that this information may not
be prior art, and/or to prove that this information may not be
enabling for the teachings purportedly offered.

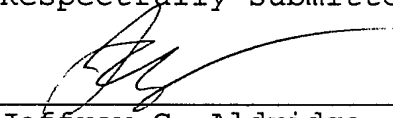
The above-identified documents were previously cited in an Information Disclosure Statement filed on September 26, 2006, and were not considered by the Examiner. These references are being re-submitted herewith for the Examiner's consideration.

It is respectfully requested that these references be: (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent which may issue on this application. Applicant requests that a copy of Form PTO-SB/08, as considered and initialed by the Examiner, be returned with the next communication.

This Statement is submitted with a Request for Continued Examination under 37 C.F.R. § 1.114. The Director is hereby authorized to charge \$180.00 in payment of the fee for submission of this Information Disclosure Statement pursuant to 37 C.F.R. §1.97(c)(2), payment of any additional fees required in connection with this Statement, or to credit any overpayment of the same, to Deposit Account No. 06-1075 (Order No.: 001202-0127). A duplicate copy of this Information Disclosure Statement is enclosed herewith.

An early and favorable action is respectfully requested.

Respectfully submitted,



Jeffrey C. Aldridge
Registration. No. 51,390
Agent for Applicant
Fish & Neave IP Group
Ropes & Gray LLP
Customer No. 1473
1211 Avenue of the Americas
New York, New York 10036
Tel.: (212) 596-9000
Fax: (212) 596-9090

Express Mail Label No. EK708051943U



PTO/SB/08a/b (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to furnish information to the collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/672,961 (Conf. No. 9439)
				Filing Date	September 26, 2003
				First Named Inventor	Glenn J. Leedy
				Art Unit	2822
				Examiner Name	Monica Lewis
Sheet	1	of	1	Attorney Docket Number	ELM-2 CONT. 4

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		DE3233195	03-17-1983	Mitsubishi Electric Corp.		
		JP2037655	02-07-1990	Siemens AG		
		JP3127816	05-30-1991	Canon KK		
		JP3174715	07-29-1991	Fujitsu Limited		
		WO 98/19337	05-07-1998	Trusi Technologies, LLC		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Wolf, Stanley and Richard N. Tauber; Silicon Processing For the VLSI Era, Volume 1: Process Technology; Sunset Beach, CA: Lattice Press, 1986, pages 191-194	
		European Search Report for Application No. EP 02 00 9643 (date completed: October 8, 2002)	
		Phys. Rev. B., Condens. Matter Mater. Phys. (USA), Physical Review B (Condensed Matter and Materials Physics), 15 March 2003, APS through AIP, USA.	
		S. Wolf, Silicon Processing For the VLSI Era, 1990, Lattice Press, Volume 2, page 191.	
		Svechnikov, S.V.; Kobylatskaya, M.F.; Kimarskii, V.I.; Kaufman, A.P.; Kuzolev, Yu. I.; Cherepov, Ye. I.; Fomin, B.I.; "Switching Card With Aluminum Membrane Current Lead Crossovers"; 1972	
		Salazar, M.; Wilkins, C.W., Jr.; Ryan, V.W.; Wang, T.T.; "Low stress films of cyclized polybutadiene dielectrics by vacuum annealing"; Oct. 21-22, 1986; pp. 96-102.	
		Allen, Mark G.; Senturia, Stephen D.; "Measurement of polyimide interlayer adhesion using microfabricated structures"; 1988; pp. 352-356.	
		Kochugova, I.V.; Nikolaeva, L.V.; Vakser, N.M., (M.I. Kalinin Leningrad Polytechnic Institute (USSR); "Electrophysical investigation of thin-layered inorganic coatings"; 1989; pp. 826-828.	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

Express Mail Label No. EK708051943US



PTO/SB/08a/b (08-03)
 Approved for use through 07/31/2006. OMB 0651-0031
 Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/672,961 (Conf. No. 9439)
				Filing Date	September 26, 2003
				First Named Inventor	Glenn J. Leedy
				Art Unit	2822
				Examiner Name	Monica Lewis
Sheet	1	of	1	Attorney Docket Number	ELM-2 CONT. 4

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		DE3233195	03-17-1983	Mitsubishi Electric Corp.		
		JP2037655	02-07-1990	Siemens AG		
		JP3127816	05-30-1991	Canon KK		
		JP3174715	07-29-1991	Fujitsu Limited		
		WO 98/19337	05-07-1998	Trusi Technologies, LLC		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Wolf, Stanley and Richard N. Tauber; Silicon Processing For the VLSI Era, Volume 1: Process Technology; Sunset Beach, CA: Lattice Press, 1986, pages 191-194	
		European Search Report for Application No. EP 02 00 9643 (date completed: October 8, 2002)	
		Phys. Rev. B., Condens. Matter Mater. Phys. (USA), Physical Review B (Condensed Matter and Materials Physics), 15 March 2003, APS through AIP, USA.	
		S. Wolf, Silicon Processing For the VLSI Era, 1990, Lattice Press, Volume 2, page 191.	
		Svechnikov, S.V.; Kobylatskaya, M.F.; Kimarskii, V.I.; Kaufman, A.P.; Kuzolev, Yu. I.; Cherepov, Ye. I.; Fomin, B.I.; "Switching Card With Aluminum Membrane Current Lead Crossovers"; 1972	
		Salazar, M.; Wilkins, C.W., Jr.; Ryan, V.W.; Wang, T.T.; "Low stress films of cyclized polybutadiene dielectrics by vacuum annealing"; Oct. 21-22, 1986; pp. 96-102.	
		Allen, Mark G.; Senturia, Stephen D.; "Measurement of polyimide interlayer adhesion using microfabricated structures"; 1988; pp. 352-356.	
		Kochugova, I.V.; Nikolaeva, L.V.; Vakser, N.M., (M.I. Kalinin Leningrad Polytechnic Institute (USSR); "Electrophysical investigation of thin-layered inorganic coatings"; 1989; pp. 826-828.	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--